

International Journal of Microelectronics Engineering (IJME)



**International Journal of
Microelectronics Engineering**

ISSN 2455-2925

IJME

October 2016, Volume 2, Number 4

AIRCC Publishing Corporation

<http://airccse.com/ijme/index.html>

SCOPE OF THE JOURNAL

International Journal of Microelectronics Engineering invites high quality manuscripts in the research areas of analog and digital circuit design methodologies. This journal provides a worldwide, regular and comprehensive update on microelectronics engineering. Journal invites significant research and application papers in all the areas listed below. The comprehensive review papers covering recent developments will also be considered.

Topics of interest include, but are not limited to, the following:

- Semiconductor Devices and Modeling
- Mixed-Domain Simulation and Design
- Microelectronics and VLSI Circuits
- Deposition/Growth, Photolithography/Patterning
- Device Simulation and Modeling of VLSI/CMOS/Unconventional Devices
- Emerging and Advanced Fabrication Methodologies
- Logic, Architectural and System Level Synthesis
- Area, Power and Thermal Evaluation
- New Device Structures
- Nonconventional Devices: OTFTs, CNTFETs, Multigate FETs Tunnel FETs
- Display Technology
- Organic and Molecular Flexible Electronics
- Embedded Systems
- Emerging Devices and Circuits
- Design of Semiconductor Memories
- System on Chip Design
- Testing, Design for Testability
- Low Power VLSI Design
- Quantum Computing and Nano-Technology
- Micro Sensors and MEMS
- Beyond CMOS Devices

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All manuscripts will be subject to a well established, fair, unbiased peer review and refereeing procedure, and are considered on the basis of their significance, novelty and usefulness to the Journals readership. The reviewing structure will always ensure the anonymity of the referees & it will be reviewed by 3 experts in the field. The review output will be one of the following decisions.

1. Accept
2. Accept with minor changes
3. Weak Accept with major changes
4. Reject

The review process may take approximately two ~three months to be completed. The Editor reserves the right to reject a paper if it does not meet the aims and scope of the journal, it is not revised well. Not revised well.

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